

# MODULE DESCRIPTION FORM

## نموذج وصف المادة الدراسية

Module Information			
معلومات المادة الدراسية			
Module Title	Electricity Basics		Module Delivery
Module Type	S		<input checked="" type="checkbox"/> Theory <input checked="" type="checkbox"/> Lecture <input checked="" type="checkbox"/> Lab <input type="checkbox"/> Tutorial <input type="checkbox"/> Practical <input type="checkbox"/> Seminar
Module Code	FETE356		
ECTS Credits	4		
SWL (hr/sem)	100		
Module Level	3	Semester of Delivery	
Administering Department	Fuel and Energy Technologies Engineering	College	Shatt Al-Arab University College
Module Leader	Hajir Sahib Shnawa	e-mail	hajer.saheb@gmail.com
Module Leader's Acad. Title	Assistant Lecturer	Module Leader's Qualification	M.Sc
Module Tutor	Hajir Sahib Shnawa	e-mail	Email
Peer Reviewer Name		e-mail	
Scientific Committee Approval Date		Version Number	
Relation with other Modules			
العلاقة مع المواد الدراسية الأخرى			
Prerequisite module	None	Semester	
Co-requisites module	None	Semester	

### Module Aims, Learning Outcomes and Indicative Contents

أهداف المادة الدراسية ونتائج التعلم والمحتويات الإرشادية

<p><b>Module Aims</b> أهداف المادة الدراسية</p>	<ol style="list-style-type: none"> <li>1. This is the basic subject for all electrical and electronic circuits.</li> <li>2. This course deals with the basic concept of electrical circuits.</li> <li>3. To understand voltage, current and power from a given circuit.</li> <li>4. To develop problem solving skills and understanding of circuit theory through the application of techniques.</li> <li>5. To understand Kirchhoff's current and voltage Laws problems.</li> </ol>
<p><b>Module Learning Outcomes</b> مخرجات التعلم للمادة الدراسية</p>	<p>Upon completion of the course, students should be able to:</p> <ol style="list-style-type: none"> <li>1. Define Ohm's law.</li> <li>2. List the various terms associated with electrical circuits.</li> <li>3. Recognize how electricity works in electrical circuits.</li> <li>4. Describe electrical power, charge, and current.</li> <li>5. Explain the two Kirchoff's laws used in circuit analysis.</li> <li>6. Discuss the various properties of resistors, capacitors, and inductors.</li> <li>7. Discuss the operations of sinusoid and phasors in an electric circuit.</li> </ol> <p>Identify the capacitor and inductor phasor relationship with respect to voltage and current.</p>
<p><b>Indicative Contents</b> المحتويات الإرشادية</p>	<p>Indicative content includes the following.</p> <p>DC circuits – Current and voltage definitions, Passive sign convention and circuit elements, Combining resistive elements in series and parallel. Kirchhoff's laws and Ohm's law. Anatomy of a circuit, Network reduction. [15 hrs]</p> <p>AC circuits I – Time dependent signals, average and RMS values. Capacitance and inductance, energy storage elements, simple AC steady-state sinusoidal analysis. [15 hrs]</p> <p>AC Circuits II - RL, RC and RLC circuits - Frequency response of RLC circuits, simple filter and band-pass circuits, resonance and Q-factor, use of Bode plots, use of differential equations and their solutions. Time response (natural and step responses). Introduction to second order circuits. [15 hrs]</p> <p>Revision problem classes. [6 hrs]</p> <p>Resistive networks, voltage and current sources, Thevenin equivalent circuits, current and voltage division, input resistance, output resistance, maximum power transfer, RMS and power dissipation, current limiting and over voltage protection. [15 hrs]</p>

## Learning and Teaching Strategies

### استراتيجيات التعلم والتعليم

<b>Strategies</b>	Assessment is based on hand-in assignments, participation in the exercises, classes interactive tutorials, Quizzes and Practical testing
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## Student Workload (SWL)

### الحمل الدراسي للطالب

<b>Structured SWL (h/sem)</b> الحمل الدراسي المنتظم للطالب خلال الفصل		<b>Structured SWL (h/w)</b> الحمل الدراسي المنتظم للطالب أسبوعيا	4
<b>Unstructured SWL (h/sem)</b> الحمل الدراسي غير المنتظم للطالب خلال الفصل		<b>Unstructured SWL (h/w)</b> الحمل الدراسي غير المنتظم للطالب أسبوعيا	2
<b>Total SWL (h/sem)</b> الحمل الدراسي الكلي للطالب خلال الفصل	100		

## Module Evaluation

### تقييم المادة الدراسية

		Time/Number	Weight (Marks)	Week Due	Relevant Learning Outcome
Formative assessment	Quizzes	4	10% (10)	3,6, 9,12	
	Assignments	2	10% (10)	6, 12	
	Projects / Lab.	1	10% (10)	Continuous	
	Report/ Lab.	1	10% (10)	14	
Summative assessment	Midterm Exam	2 hr	10% (10)	7	
	Final Exam	4 hr	50% (50)		
Total assessment			100% (100 Marks)		

## Delivery Plan (Weekly Syllabus)

### المنهاج الاسبوعي النظري

<b>Material Covered</b>
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<b>Week 1</b>	Resistance, conductance, effect of temp. on the resistance value
<b>Week 2</b>	Ohm's law, series connection, parallel connection, compound connection
<b>Week 3</b>	Voltage and current divider solved examples, kirchhoff's laws
<b>Week 4</b>	Star-delta conversion examples
<b>Week 5</b>	Thevenin's theorem, maximum power transfer
<b>Week 6</b>	Nodal method, superposition
<b>Week 7</b>	Alternating voltage and current
<b>Week 8</b>	Frequency, period, instantaneous value of voltage and current
<b>Week 9</b>	Component of A.C circuit, pure resistance, pure inductance, pure capacitance
<b>Week 10</b>	Series A.C circuit, R,L,C in series
<b>Week 11</b>	Impedance, phase angle, resonance, phase diagram
<b>Week 12</b>	Parallel A.C circuit, R,L,C, Admittance, power factor
<b>Week 13</b>	Active, reactive, apparent power in A.C circuit
<b>Week 14</b>	3-phase circuit
<b>Week 15</b>	<b>Final Exam</b>

### Delivery Plan (Weekly Lab. Syllabus)

المنهاج الاسبوعي للمختبر

	<b>Material Covered</b>
<b>Week 1</b>	Lab 1: Using Multimeter to measure Voltage, Current and Resistance
<b>Week 2</b>	Lab 2: Ohm's law.
<b>Week 3</b>	Lab 3: Voltage and current divider rules
<b>Week 4</b>	Lab 4: Kirchhoff's laws
<b>Week 5</b>	Lab 5: Thevenin's Theorem
<b>Week 6</b>	Lab 6: Series RLC circuit
Week 7	Lab 7: Parallel RLC circuit
Week 8	

## Learning and Teaching Resources

### مصادر التعلم والتدريس

	Text	Available in the Library?
<b>Required Texts</b>	DC Electrical Circuit Analysis: A Practical Approach, 2020.	No
<b>Recommended Texts</b>	DC Electrical Circuit Analysis: A Practical Approach, 2020	no
<b>Websites</b>	<a href="https://docs.google.com/file/d/0B_O5jg0LZ_ZXYlg0WVU1bkhrLTg/edit">https://docs.google.com/file/d/0B_O5jg0LZ_ZXYlg0WVU1bkhrLTg/edit</a>	

## Grading Scheme

### مخطط الدرجات

Group	Grade	التقدير	Marks (%)	Definition
<b>Success Group (50 - 100)</b>	<b>A</b> - Excellent	امتياز	90 - 100	Outstanding Performance
	<b>B</b> - Very Good	جيد جدا	80 - 89	Above average with some errors
	<b>C</b> - Good	جيد	70 - 79	Sound work with notable errors
	<b>D</b> - Satisfactory	متوسط	60 - 69	Fair but with major shortcomings
	<b>E</b> - Sufficient	مقبول	50 - 59	Work meets minimum criteria
<b>Fail Group (0 – 49)</b>	<b>FX</b> – Fail	راسب (قيد المعالجة)	(45-49)	More work required but credit awarded
	<b>F</b> – Fail	راسب	(0-44)	Considerable amount of work required

**Note:** Marks Decimal places above or below 0.5 will be rounded to the higher or lower full mark (for example a mark of 54.5 will be rounded to 55, whereas a mark of 54.4 will be rounded to 54. The University has a policy NOT to condone "near-pass fails" so the only adjustment to marks awarded by the original marker(s) will be the automatic rounding outlined above.